

Amendments to the Claims:

This listing of claims will replace all prior versions, and listings of claims in the application:

Listing of Claims:

1 1. (currently amended) A control apparatus of a storage unit, the control
2 apparatus having a first communication port for conducting communication with a computer, a
3 first processor that controls the first communication port, a first storage device that stores a first
4 queue for storing a command for conducting the communication sent from the computer to the
5 first communication port, a first memory that the first processor accesses, a second
6 communication port for conducting communication with the computer, a second processor that
7 controls the second communication port, and a second storage device that stores a second queue
8 for storing a command for conducting communication sent from the computer to the second
9 communication port, the first processor executing the command stored in the first queue to
10 thereby control the communication with the computer, the second processor executing the
11 command stored in the second queue to thereby control the communication with the computer,
12 the control apparatus comprising:
13 a unit causing the second processor to implement execution of the command
14 stored in the first queue; and
15 a-said unit changing data stored in the first memory while the second processor is
16 being caused to implement execution of the command stored in the first queue.

1 2. (original) A control apparatus of a storage unit according to claim 1,
2 wherein the unit causing the second processor to manage execution of the command stored in the
3 first queue is a unit allowing the first processor to transfer the command stored in the first queue
4 to the second queue and allowing the second processor to read the command for execution from
5 the second queue.

1 3. (original) A control apparatus of a storage unit according to claim 1,
2 wherein the unit causing the second processor to manage execution of the command stored in the

3 first queue is a unit allowing the second processor to read the command for execution from the
4 first queue.

1 4. (original) A control apparatus of a storage unit according to claim 1,
2 wherein the unit changing data stored in the first memory is a unit allowing the first processor to
3 write data inputted from the outside over data stored in the first memory.

1 5. (currently amended) ~~A method of controlling a control apparatus of a~~
2 ~~storage unit, the communication port computer, a first communication port, a first queue for~~
3 ~~control apparatus having a first for conducting communication with a processor that controls the~~
4 ~~first a first storage device that stores storing a command for conducting communication port, A~~
5 method of controlling a control apparatus of a storage unit, a communication port computer, a
6 first communication port, a first queue for the control apparatus having a first communication
7 port for conducting communication with a processor that controls the first storage device that
8 stores a command for conducting communication with the first communication port, a first
9 nonvolatile memory that the first processor accesses, a second communication port for
10 conducting communication with the computer, a second processor that controls the second
11 communication port, and a second storage device that stores a second queue for storing a
12 command for conducting communication sent from the computer to the second communication
13 port, the first processor executing the command stored in the first queue to thereby control the
14 communication with the computer, the second processor executing the command stored in the
15 second queue to thereby control the communication with the computer,
16 the method comprising:
17 causing the second processor to implement execution of the command stored in
18 the first queue; and
19 changing data stored in the first nonvolatile memory while the second processor is
20 being caused to implement the execution of the command stored in the first queue.

1 6. (previously presented) A method according to claim 5, wherein causing
2 the second processor to implement execution of the command stored in the first queue comprises
3 allowing the first processor to transfer the command stored in the first queue to the second queue
4 and allowing the second processor to read the command for execution from the second queue.

1 7. (previously presented) A method according to claim 5, wherein causing
2 the second processor to implement execution of the command stored in the first queue comprises
3 allowing the second processor to read the command for execution from the first queue.

1 8. (previously presented) A method according to claim 5, wherein changing
2 data stored in the first memory comprises allowing the first processor to write data inputted from
3 the outside over data stored in the first memory.